Camera link Splitter



SCL-100T3

(Base Configuration)



Description

Syscom Korea, a leader in the market of pixel to pixel cabling solution, created Fiber Optics based Camera Link cable. Syscom Korea cabling solution will be a perfect choice for overcoming the problems of facility access, capital cost, distance, EMI, ESD and bandwidth. Camera Link is a highspeed camera/frame grabber interface designed for high performance vision applications. Standard Camera Links deliver signals to 10 meters or less, occurring inflexible situations in many applications which requires longer distance image transmission between the camera and the frame grabber.

The SCL-100T3 Camera Link Splitter interfaces one Camera Link camera to two frame grabbers using standard Camera Link cables. This arrangement enables the addition of a second frame grabber for functions such as camera setup, secondary/parallel processing, monitoring, and recording. One frame grabber acts as master and provides control and communications to the camera. The second frame grabber receives camera video data only.

Key Features

- All of Camera-Link Interface Compliance (Area and Line Scan)
- Support Base, Medium and Full Configuration
- Support Pixel clock from 20 85MHz
- Zero loss and transparent transport
- Locking DC power connector
- Attaches to the compact MDR26 connect

Applications

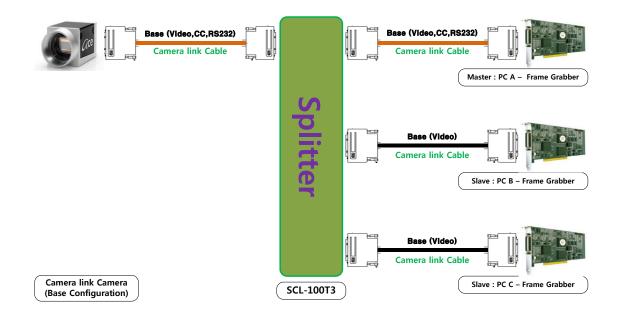
- Machine Vision Applications
- Product inspection PCB, LCD, Wafer
- Bar code reading and sorting
- Medical Imaging
- Port/Harbor cargo container management
- Railroad measurement/inspection
- Cameras placed in harsh environment
 Nuclear plant, Chemical plant, Factory
- Bridge inspection
- Military & Defense Applications
- Astronomy
- Computer microscopy
- Multi-media

Tel:+82-31-7070-222, Fax:+82-31-7070-220, eMail : choosyscom@daum.net, No.406 Banpo Technopia, 186 Galmachi-ro , Jungwon-gu, SeongNam-si GyeongGi-do, Seoul Korea





Typical Set Up Diagram



Technical Specifications

- General Specification

Operating Temperature	0 ~ 50 [°C]
Input Voltage	DC 12 ~ 24 [V]
Typical Supply Current @ 12V DC	150 [mA]
Connector Type	Molex 53259-0329 Male, DC Jack
Weight (Approximate)	300 Gram

- Camera link Interface

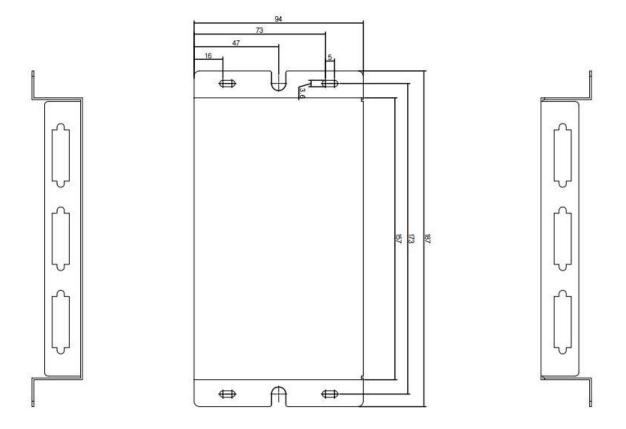
Supported Camera Configuration	Base
Pixel Clock Range	20 ~ 85 [MHz]
Sync. Signals	LVAL, DVAL, FVAL
Camera Control - Master only	CC1 ~ CC4
Serial Communication	SerTFG, SerTC
Connector Type	Camera Link (MDR)

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Dimensions



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